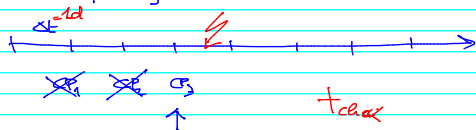


FAULT TOLERANCE

NF CONCERN

PM $\xrightarrow{\text{LONG RUNNING (h} \rightarrow \text{d)}}$ t

Snapshot of the state
 $\rightarrow PC \rightarrow \text{where}$
 $\{MEM\} \rightarrow \text{state (what)}$ } CHECKPOINT

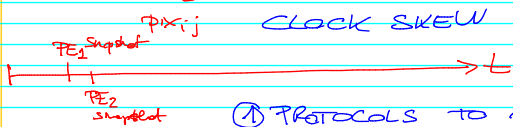
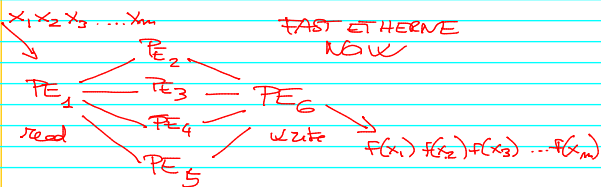


$$t_{clear}$$

$$T_{seq} + M T_{clock}$$

$$\downarrow$$

$$T_{seq} / \Delta t$$

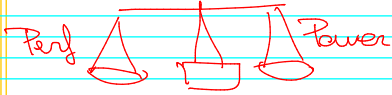
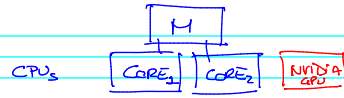


① PROTOCOLS TO AGREE ON WHEN WE DO CHECKPOINTS

② PC + STATE + COMMS } \rightarrow SNAPSHOT

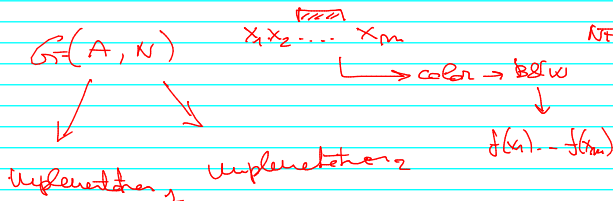
POWER MANAGEMENT

Mobile systems \longleftrightarrow HPC
 (\rightarrow batteries) (\rightarrow cost of power)



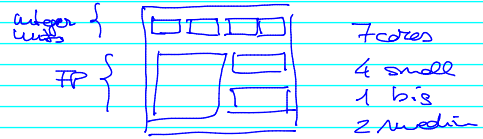
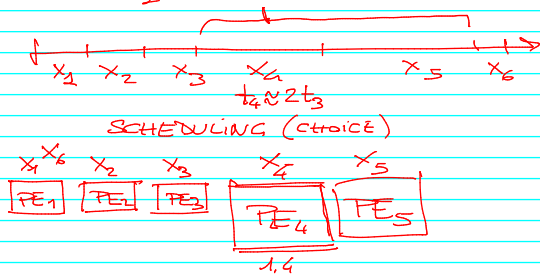
$$\forall_i: \text{img}_i \xrightarrow{f} \text{img}'_i \quad \begin{matrix} \text{CPU } f \\ \text{GPU } f \end{matrix}$$

img_i move it to CPU(H)
 f on GPU
 move back img'_i to CPU(H)



RESEARCH PROJECTS

HETEROGENEOUS MULTI CORES

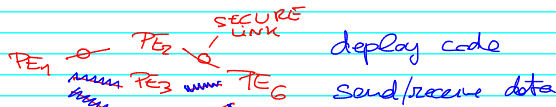


SECURITY

NDW / COW PUBLIC NETWORK SEGMENT

GRIDS


SECURE $\left\{ \begin{array}{l} \text{DATA} \\ \text{CODE} \end{array} \right.$

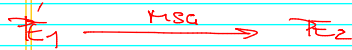


deploy code
send/receive data

PLAIN sock \rightarrow send
SSL sock \rightarrow send +
 + receive

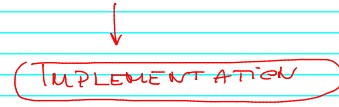
TE1 \equiv TCP/IP PORT X

SEC \rightarrow  SSL sockets instead of PLAIN TCP/IP sockets



PubKey₂(K) \rightarrow PrivateKey₂(PubKey₂(K)) \rightarrow K

Code . Act . GRAPA

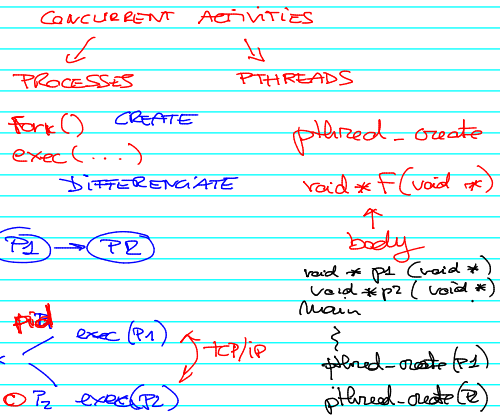
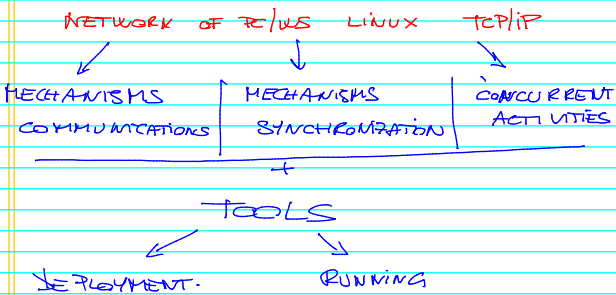


sec columns for insecure links

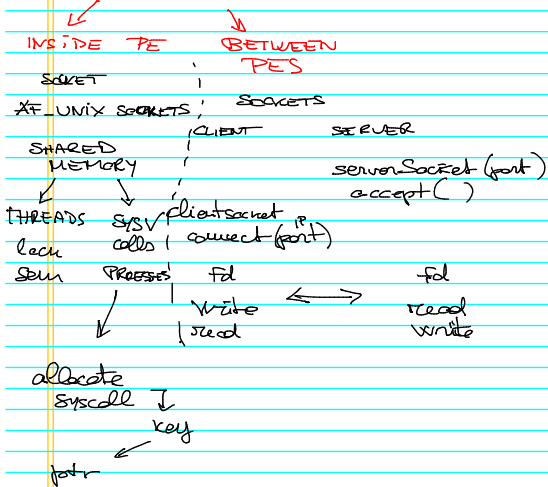
N sec / hostn columns for secure links

deploy(x) \rightarrow secure nodes
deploy sec(x) \rightarrow N secure nodes
 \uparrow
 x x_{sec}

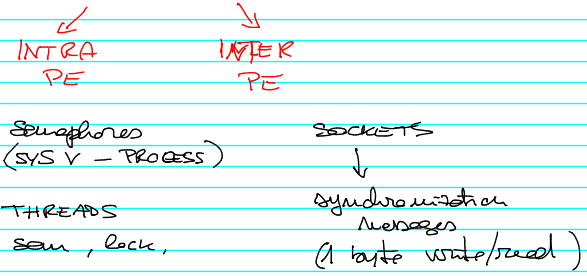
CLASSICAL // PROGRAMMING FRAMEWORKS



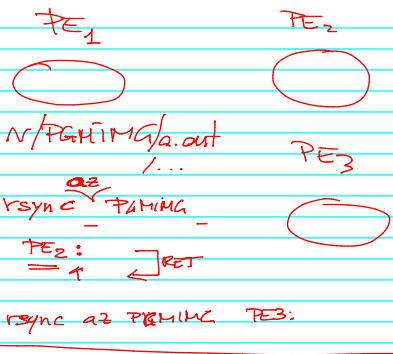
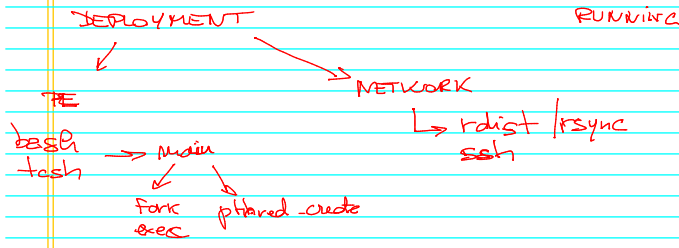
COMMUNICATIONS



SYNCHRONIZATION



Tools



> rdist distfile

disting: {PE₁, PE₂} files
↳ install ...
distfile