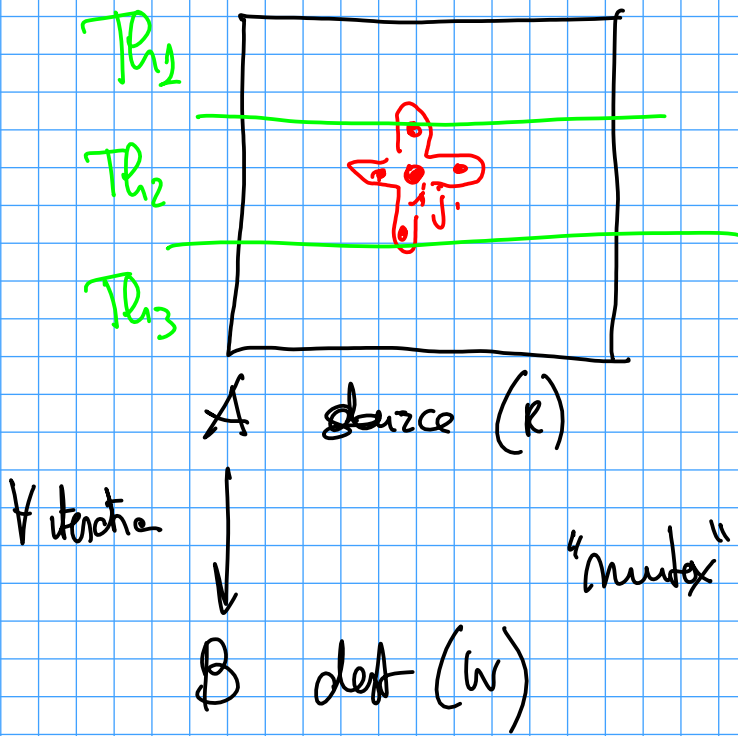


Synchronization



Stencil computation
↓
owner computes rule

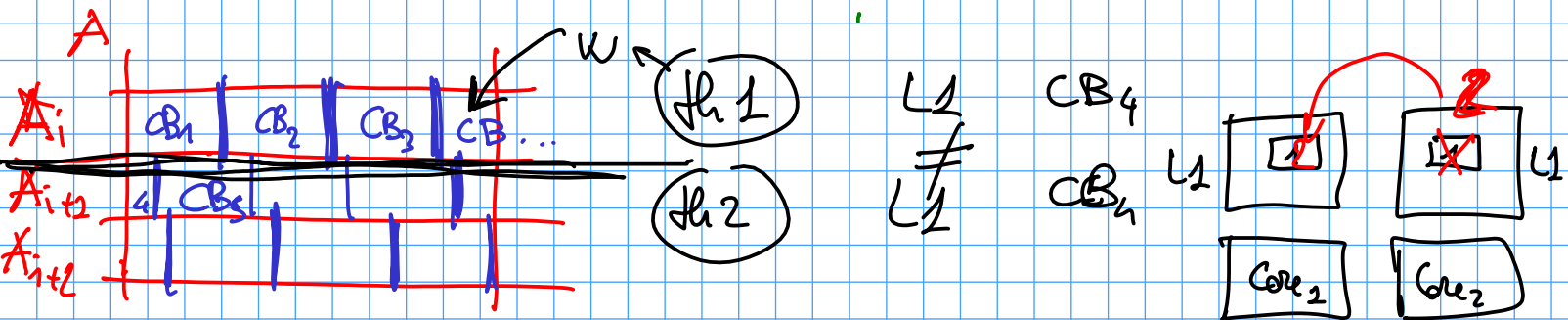
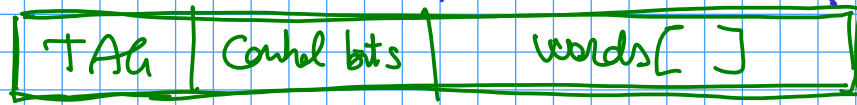
```

for (int j = 0; j < endrow; j++)
{
    for (int i = startrow[j]; i < endrow[j]; i++)
    {
        X[i][j] = ... X[i][j] ...
    }
}
    
```

lock [N][N]

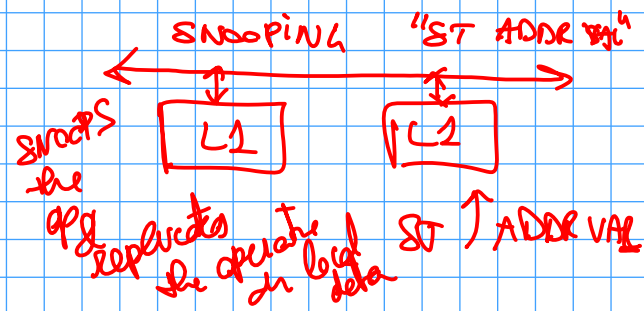
→ try to avoid any kind of implicit synchronization

cache line



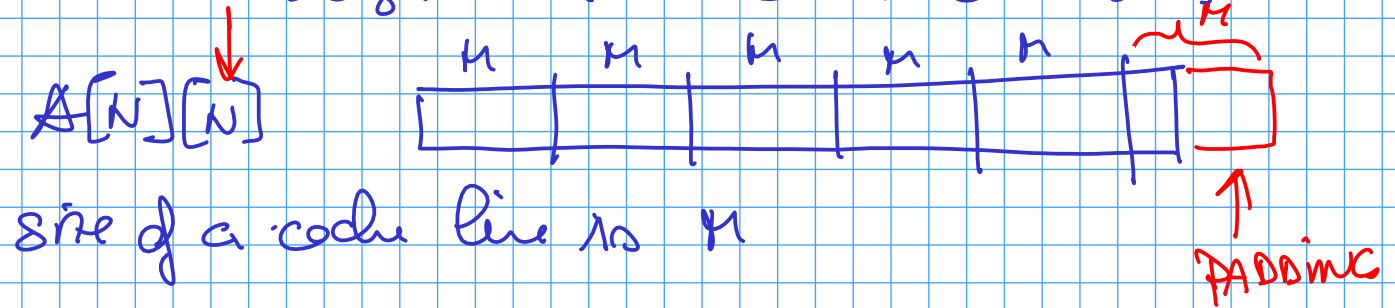
cache coherence protocol

directory based



⇒ PADDING TECHNIQUES

↓ align data to cache boundaries



$$A[N] \left[\begin{array}{c} N \\ \hline M \end{array} \right]$$

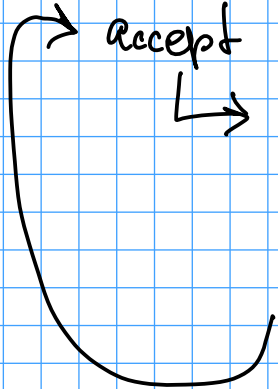
CLIENT SERVER

Server ::

socket create
bind socket
(listen)

accept

↳ socket read req
write answers
close



MT Server ::

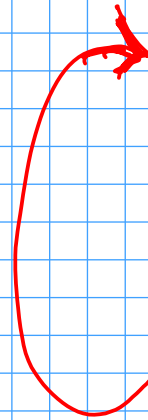
socket create
bind socket
(listen)

accept

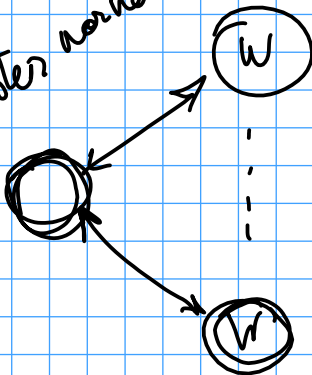
↳ socket ^{pthread_create}
fork process

↳ read
writes
close
exits

pid



Master worker



CoW/Now
(MC nodes)

processes on a MC Machine
MT server

MC

Machine

Worker :: receive (task)
↓
compute (task) → res
↓
results (res)

Server

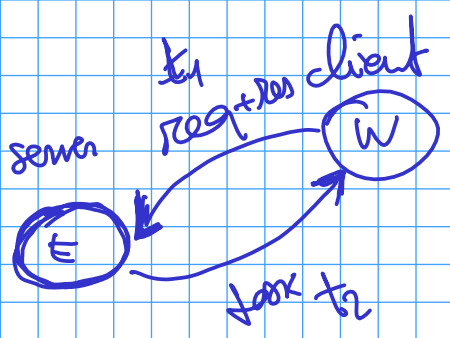
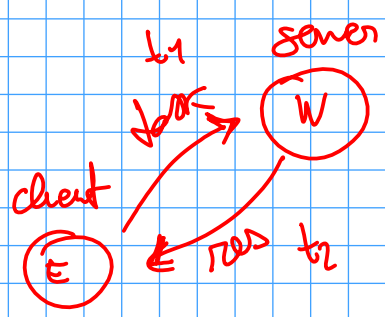
4 cores → ALTERNATIVE 1
placing 4 "workers" on 4 diff parts

ALTERNATIVE 2 (the good one)

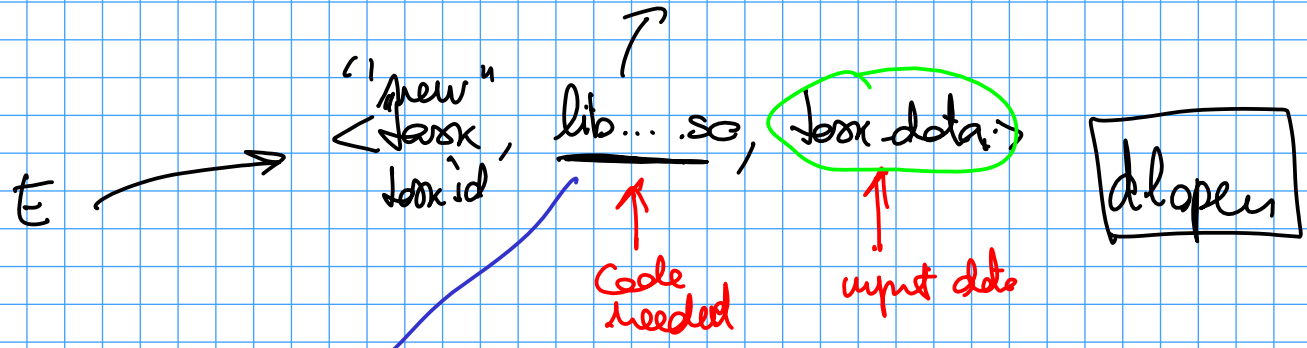
place a server (MT!)
where you use a thread pool with 4 th

ALTERNATIVE 3 (the best one)

Worker Server :: socket / bind / listen
 prepare the server socket
 for(...) pthread_create(...)
 prepares the thread pool
 while(...) {
 s = accept(...);
 activate 1 thread from the pool
 }



Task compute (T in t)



Subject to optimization
 → first time → send code
 → i ... n → send a ref to code

W : load .so library
 uncore compute out the
 input data
 res =
 send back(res)